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JP8171526A2: INPUT/OUTPUT INTERFACE DEVICE AND ITS CONTROL METHOD

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Country: **JP Japan**

Kind:

Inventor(s): **SUGIMOTO KINICHI**


Applicant(s): **NEC CORP**
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Issued/Filed Dates: **July 2, 1996 / Dec. 19, 1994**

Application Number: **JP1994000315242**

IPC Class: **G06F 13/10; G11B 20/10;**

Abstract: **Purpose:** To lighten the load of a real-time processing on a host computer by providing a secondary storage device of a computer with an interface which can be controlled in synchronism with a reference clock.
Constitution: The input/output interface which performs input operation to and output operation from a host computer 1 is a real-time input/output interface consisting of a bus interface control part 32 which accepts input/output process requests at constant intervals of time, a preference queue management part 33 which holds an input/output request according to the priority of a process, a schedule 31 which classifies a preference queue on the basis of the emergency of an input/output request, a reference clock generation part 2 which generates the reference clock for the operation of the input/output clock, and an input/output control part 34 which receives a process request from the preference queue management part 33 at constant intervals of time on the basis of the reference clock sent from the reference clock generation part 2 and performs input/output control.
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Other Abstract Info: **DERABS G96-311335**

Foreign References: (No patents reference this one)

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